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|-------------|--|
| Manufacture | |
| Name | |
| Logo | |
| Intro | <p>UNISOC is the world's leading chip design company. It is one of the few companies in the world that has fully mastered 2G/3G/4G/5G, Wi-Fi, Bluetooth, TV FM, satellite communications and other related technologies. In the 5G field, UNISOC is one of the three 5G chip companies in the global open market.</p> <p>UNISOC has extensive expertise in large-scale chip integration and complete peripheral chip integration. UNISOC product portfolio includes mobile communication central processing units, baseband chips, AI chipsets, radio frequency front-end chips, radio frequency chips and other communication, computing and control chips, etc. UNISOC field test covers 133+ countries and regions, and has passed shipment certification of 260+ operators worldwide. UNISOC has more than 500 customers including HONOR, realme, vivo, Samsung, moto, Hisense, ZTE, JD, UnionPay, and GREE.</p> <p>UNISOC has won the National Science and Technology Progress Award several times, including one special prize and two first prizes, as well as submitted applications for more than 11,000 patents and possesses core patents such as 3G/4G/5G, multi-card multi-standby, and multi-mode.</p> |
| Website | |
| Country | |
| Brand | |
| | 紫光展锐 |

- | | |
|------------------------|--|
| Chips | |
| Core Type | |
| ARM Cortex-A53SMp4 | |
| Dimension | |
| UART | |
| Current | |
| Transceiver Compatible | |
| Intro | |
| Debugging | |
| DSP RAM | |

Voice Services

Crystal

no

Power fail

no

Wifi

-

AoA/AoD

- 无

Operating Frequency [Max] (MHz)

SSI

Sensor Controller

Receiver Sensitivity

Upgrade Type

Manual

Package Type

-

DSP Technology

General

on chip (PLL)

no

I2S

Chip Name

W377

Flash (kB)

I2C

-

Standby

Output Power

CPU Clock Speed

Pins

DSP Clock Speed

Features

802.15.4 (Zigbee, Thread)

crystal (optional)

no

PDM

LE Audio

- 无

Type

蓝牙低功耗

Bluetooth Version

I2S

no

Frequency Regulation

CPU Features

Pitch

on-chip RC

no

PWM

Brand

恩智浦

SRAM (kB)

Real-Time Clock

no

CPU Architecture

Channels

External clock

no

Protocols

-

RADIO

SAADC

Country

中国

EEPROM (kB)

Crypto Accelerator

Programmable channels

Watchdog timer

no
TWI

no
Bluetooth 5 PHYs

-
SPIM

NFC Tag

SPI

Public Key Hardware Accelerator

Fixed channels

QDEC

-
bluetooth 5.1 support

-
SPIS

CMP

Overview

GPIO

Accelerator

Channel groups

PDM

-
Security

TWIM

Features

CAN

True Random Number Generator

USB

no
TWIS

Block Diagram

CAN FD

Monitor

SPI

no

UARTE

RAM(KB)

Application Note

Human Machine Interface

-

Quad SPI

-

NFCT

Development Board

Security Modules

Debug interface

-

LDO

no

USB

Datasheet

Timers [Number, bits]

no

On-chip VBUS

no

QSPI

PWM [Number, bits]

Image

Regulated supply for external components

-

ADC [Number, bits]

Price

DAC [Number, bits]

RMB

□

Supply Voltage [Min to Max] (V)

Rating

no 

Analog comp

no

Ambient Operating Temperature (Min to Max) (°C)

low-power comp

no

Cache

-

Junction Temperature (Min to Max) (°C)

Temperature sensor

no